POTTI SRIRAMULU CHALAVADI MALLIKHARJUNA RAO COLLEGE OF ENGINEERING & TECHNOLOGY

Sponsored by: SKPVV Hindu High School Committee,Estd: 1906 (Approved by AICTE Affiliated to JNTU Kakinada)

ISO 9001:2015 Certified

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FACULTY PROFILE

D.Srikanth
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Experience:

Teaching: 13

Industry: Nil

Career Profile

Year	Organization	Role
2013 – till date	PSCMRCET	**Assoc.Prof**
2012-13	PSCMRCET	Asst.Prof
2008-12	ASIST, Paritala	Asst.Prof
2006-08	MIST,Sathupaplli	Asst.Prof
2004-06	JSNCET, KaghazNagar	Asst.Prof

Qualification

Degree and Date	Board / University	Date of Completion	% Marks
M.Tech	JNTUK	2010	73
B.Tech	ANU	2002	65.4
INTERMEDIATE	IPE	1998	77.4
SSC	SSC Board	1996	75

The subjects handled:

Electronic Devices & Circuits
Analog Communication
Digital Communication
Signals & Systems
MicroWave Engineering
PTSP
DLD
Priniciples of Communication
OS

Certifications

Certification	Date Certified

Affiliation to Professional Bodies

MISTE : LIFE MEMBER LM 74600

ISSE : LM03704

Publications

❖ A paper titled "Establishing a Low Bandwidth and Low Latency Transceivers for Low Dense Applications" published in International Journal for Modern Trends in Science and Technology , Volume: 03, Issue No: 09, September 2017 , ISSN: 2455-3778

❖ Published a paper titled "A survey on Peak to average power ratio reduction techniques in OFDM " , IJECIERD in 2013

❖ Published a paper titled " AN enhanced and energy efficient hierarchical routing protocol in wireless sensor networks, IJITECH in 2015"

Conferences, Seminars, Workshops & Guest Lectures Attended

❖ Attended a workshop an Machine learning in speech processing at KLU conducted by NIT Warangal from 1/12/18 to 6/12/18

❖ Attended an FPD an Long term Evolution & Advances in Mobile Communication at SRK Institute conducted by UTL Technologies from 26/12/17 to 31/12/17

❖ Attended the Workshop on "5G TECHNOLOGY KNOWLEDGE & SKILLS" organized by UTL Technology Ltd in PSCMRCET on 24th August 2018.

❖ Participated in 5 day Faculty Development Program (FDP) on "INSTRUCTIONAL DESIGN & DELIVERY" held at PSCMRCET from 04.06.2018 to 08.06.2018.

❖ Participated in the event JIGNASA as department coordinator held on 12/12/2012 in PSCMRCET, Vijayawada

❖ Attended a Five - day Faculty Development Program on "Magical Teacher" organized by in PSCMRCET from 09 – 13 November 2016.

❖ Acted as a resource person for workshop on SCILAB and its Applications conducted in PSCMRCET, Vijayawada.

❖ Attended a Three - day Workshop on "PCB Design" organized by Electronics Enthusiasts Club in PSCMRCET from 10 – 12 September 2015.

❖
Attended One - day IEEE National Workshop on Understanding 5G: Perspectives on Future Technological Advancements in Wireless Communication held on 08 August 15 in PSCMRCET, Vijayawada.

❖
Attended One Week Faculty Development Program (FDP) on "Hands on Experience with HFSS" from 18 – 23 may 2015 in PSCMRCET, Vijayawada.

❖
Attended a Five - day Faculty Development Program (FDP) on "English Communication Skills" in PSCMRCET from 03 – 07 June 2013.

Events organized/ Conducted

Funding Projects Handled (If any – Please provide the details of the project)

D.Srikanth

Signature